



Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty Docket No. NSC1P300/P05882 Applicant: Kelkar Filing Date 04/14/2004	Application No.: 10/824,903 Group 2891
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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
DAZ	A1	2005-0516331	07/21/05	Akram			03/10/05
	A2	2003-0218258	11/27/03	Charles et al.			05/20/03
	A3	2005-0212142	09/29/05	Miyazaki et al.			04/28/05
	A4	2003-0193096	10/16/03	Tao et al.			01/28/03
	A5	2002-0171152	11/21/02	Miyazaki			05/20/02
	A6	2003-0169064	09/11/03	Pirkle et al.			01/15/03
	A7	2003-0129789	07/10/03	Smith et al.			01/29/03
	A8	2003-0127502	07/10/03	Alvarez			12/10/02
	A9	2002-0109228	08/15/02	Buchwalter et al.			02/13/01
	A10	2003-0099767	05/29/03	Fang			12/24/02
	A11	2003-0087475	05/08/03	Sterrett et al.			11/08/01
	A12	2003-0080360	05/01/03	Lee et al.			10/28/02
	A13	2002-0031868	03/14/02	Capote			09/07/01
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	A15	2003-0013233	01/16/03	Shibata			07/11/02
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	A17	2004-0002181	01/01/04	Scheifers et al.			04/02/03
	A18	2003-0001283	01/02/03	Kumamoto			06/29/01
	A19	5,088,189	04/16/91	Oroskar et al.			06/20/89
	A20	5,128,746	07/07/92	Pennisi et al.			09/27/99
	A21	5,329,423	07/12/94	Scholz			04/13/93
	A22	5,736,456	04/07/98	Akram			07/17/96
	A23	5,767,010	06/16/98	Mis, et al.			11/05/96
	A24	5,773,359	06/30/98	Mitchell, et al.			12/26/95
	A25	5,937,320	08/10/95	Andricacos, et al.			04/08/98
	A26	5,977,632	11/02/99	Beddingfield			02/02/98
	A27	6,327,158	12/04/01	Kelkar			01/15/99
	A28	5,136,365	08/04/92	Pennisi et al.			09/27/99
	A29	5,214,308	05/25/93	Nishiguchi et al.			01/23/91
	A30	5,244,143	09/14/93	Ference et al.			04/16/92
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	A32	5,329,423	07/12/94	Scholz			04/13/93
DAZ	A33	5,376,403	12/27/97	Capote et al.			10/01/91

Examiner /David Zarneke/ Date Considered 09/22/2006

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**Information Disclosure
Statement By Applicant**

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	A36	5,587,342	12/24/96	Lin et al.			04/03/95
	A37	5,668,059	09/16/97	Christie et al.			07/12/96
	A38	5,698,894	12/16/97	Bryant et al.			10/28/96
	A39	5,736,456	04/07/98	Akram			07/17/96
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	A42	5,773,359	06/30/98	Mitchell et al.			12/26/95
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	A44	5,880,530	03/09/99	Mashimoto et al.			03/29/96
	A45	5,895,976	04/20/99	Morrell et al.			06/03/96
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	A51	6,060,373	05/09/00	Saitoh			07/09/99
	A52	6,063,647	05/16/00	Chen et al.			12/08/97
	A53	6,071,757	06/06/00	Fogal et al.			10/20/98
	A54	6,100,114	08/08/00	Milkovich et al.			08/10/98
	A55	6,121,689	09/19/00	Capote et al.			07/21/98
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	A62	6,288,444	09/11/01	Abe et al.			06/04/99
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Examiner	/David Zarneke/			Date Considered	09/22/2006		

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	A69	6,358,627	03/19/02	Benenati et al.			01/23/00
	A70	6,372,547	04/16/02	Nakamura et al.			03/11/98
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	A73	6,455,920	09/24/02	Fukasawa et al.			09/25/98
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Examiner /David Zarneke/				Date Considered 09/22/2006			

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Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
DAZ	C1	Kulicke & Soffa, Flip Chip Division, Polymer Collar™ Wafer Level Package; "Achieve Maximum Reliability for Wafer Level Packages!", December 7, 2001, www.kns.com
	C2	Kulicke & Soffa, Flip Chip Division, Polymer Collar™ Wafer Level Package, "See the Polymer Collar™ WLP Difference!", December 7, 2001, www.kns.com .
	C3	Lau et al., "Chip Scale Package: Design, Materials, Process, Reliability, and Applications," February 28, 1999, McGraw-Hill Professional Publishing; Chapter 1, pages 1-41.
	C4	Rao R. Tummala, "Fundamentals of Microsystem Packaging," May 8, 2001, McGraw-Hill Professional Publishing; Chapters 2, 10 and 17.
	C5	"Polymer Collar WLP™, Wafer Level Package Family", © 2002, downloaded from www.kns.com/prodserv/PDFS/FCD/polymer_collar.pdf , 2 pages.
	C6	"Presenting Polymer Collar WLP™ – A New Wafer Level Package for Improved Solder Joint Reliability", © 2002, downloaded from www.kns.com/prodserv/flipchip/pdf/PC_ad.pdf , 1 page.
	C7	Barrett et al., Kulicke & Soffa, "Polymer Collar WLP™ – A New Wafer Level Package for Improved Solder Joint Reliability", © 2002, downloaded from www.kns.com/resources/articles/PolymerCollar.pdf , 9 pages.
	C8	Bogatin, Eric, "All Dressed Up and Nowhere to Go"; Semiconductor International, May 1, 2002, downloaded December 23, 2003, from www.reed-electronics.com/semiconductor/index.asp?layout=article@articleid=CA213812@rid=0&rme=0&cfd=1 , 2 pages.
DAZ	C9	Nguyen et al., "Effect of Underfill Fillet Configuration on Flip Chip Package Reliability", SEMI® Technology Symposium: International Electronics Manufacturing Technology (IEMT) Symposium, SEMICON® West 2002.
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Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
DAZ	C10	U.S. Patent Application 10/080,913, filed February 21, 2002.
✓	C11	U.S. Patent Application 10/224,291, filed August 19, 2002.
DAZ	C12	U.S. Patent Application 10/707,208, filed November 26, 2003.
Examiner DAZ	Date Considered /David Zarneke/	

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